



PTO/SB/08A (10-01)
Approved for use through 10/31/2002. OMB 0651-0031
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Project of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	09/815,921
		Filing Date	March 24, 2001
		First Named Inventor	Bindiganavale S. Nataraj
		Art Unit	2661
		Examiner Name	Not Yet Assigned
Sheet 1	of 7	Attorney Docket Number	NLM.P002

U.S. PATENT DOCUMENTS					
Examiner Initials ¹	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
CLD		US- 3,257,646	06-21-1966	Roth	
CLD		US- 3,353,159	11-14-1967	Lee, III	
CLD		US- 3,602,899	08-31-1971	Lindquist et al.	
CLD		US- 3,675,211	07-04-1972	Raviv	
CLD		US- 3,685,020	08-15-1972	Meade	
CLD		US- 3,868,642	02-25-1975	Sachs	
CLD		US- 4,112,502	09-05-1978	Scheuneman	
CLD		US- 4,244,033	01-06-1981	Hattori	
CLD		US- 4,472,805	09-18-1984	Wacyk et al.	
CLD		US- 4,523,301	06-11-1985	Kadota	
CLD		US- 4,646,271	02-24-1987	Uchiyama	
CLD		US- 4,656,626	04-07-1987	Yudichak et al.	
CLD		US- 4,670,858	06-02-1987	Almy	
CLD		US- 4,747,080	05-24-1988	Yamada	
CLD		US- 4,758,982	07-19-1988	Price	
CLD		US- 4,780,845	10-25-1988	Threewitt	
CLD		US- 4,785,398	11-15-1988	Joyce et al.	
CLD		US- 4,791,606	12-13-1988	Threewitt et al.	
CLD		US- 4,813,002	03-14-1989	Joyce et al.	
CLD		US- 4,845,668	07-04-1989	Sano et al.	

RECEIVED

AUG 14 2002

Technology Center 2600

FOREIGN PATENT DOCUMENTS						
Examiner Initials ¹	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
CLD		EP 0 267 612 A2	05-18-1988	Akita		
CLD		EP 0 381 249 A2	08-08-1990	Akashi		
CLD		EP 0 660 332 A1	06-28-1995	Montoye		
CLD		EP 0 774 758 A2	05-21-1997	Rao		
CLD		EP 0 872 802 A2	10-21-1998	Kaganoi		
CLD		EP 08167295 (Japan Abst)	06-25-1996	Hiroshi		
CLD		EP 08273376 (Japan Abst)	10-18-1998	Takeshi		
CLD		WO 98/12651	03-26-1998	Kemoko		
CLD		WO 99/31581	06-24-1999	Therkildsen		

Examiner Signature		Date Considered	1/10/05
-----------------------	--	--------------------	---------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.**

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.**



PTO/SB/088 (10-01)
Approved for use through 10/31/2002. OMB 0651-0031
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449B/PTO		Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Application Number	09/815,921
		Filing Date	March 24, 2001
		First Named Inventor	Bindiganavale S. Nataraj
		Group Art Unit	2661
		Examiner Name	Not Yet Assigned
		Attorney Docket Number	NLM.P002
Sheet	5	of	7

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
CLD		Advanced Micro Devices, "Final Am99C10A 256 x 48 Content Addressable Memory" Publication No. 08125, Rev. G, December 1992, pp. 1-21.	
CLD		MASAO AKATA, "A Scheduling Content-Addressable Memory for ATM Space-Division Switch Control", IEEE International Solid-State Circuits Conference, Feb. 1991, New York.	
CLD		SOO-IK CHAE et al., "Content-Addressable Memory for VLSI Pattern Inspection", IEEE Journal of Solid State Circuits, Vol. 23, No. 1, February 1988, pp. 74-78.	
CLD		Cisco Systems, Inc., "Policy-Based Routing", 1996, pp. 1-7.	
CLD		GEC Plessey Semiconductors, "P2800 Multi-Port Content Addressable Memory", Functional Product Specification GPS-FPS-2800-12, January 1986.	
CLD		GEC Plessey Semiconductors, "P2800 2K x 64 Bit Multi-Port Content Addressable Memory", Preliminary Information, February 1997, pp. 1-15.	
CLD		KANAD GHOSE, "Response Pipelined CAM Chips: The First Generation and Beyond.", 7th International Conference on VLSI Design, January 1994, pp. 365-368.	
CLD		PANKAJ GUPTA et al., "Routing Lookups in Hardware at Memory Access Speeds", Proc. Infocom, April 1988, 8 pgs.	
CLD		PANKAJ GUPTA and NICK MCKEOWN, "Packet Classification on Multiple Fields", SIGCOMM (September 2, 1999), 14 pages.	
CLD		SIMON R. JONES et al., "A 9-kbit Associative Memory for High Speed Parallel Processing Applications", IEEE Journal of Solid-State Circuits, Vol. 23, No. 2, April 1988, pp. 543-548.	
CLD		HIROSHI KADOTA et al., "An 8-kbit Content-Addressable and Reentrant Memory", IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 5, October 1985, pp. 951-956.	

Examiner Signature		Date Considered	8/19/05
-----------------------	--	--------------------	---------

¹ EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

² Applicant's unique citation designation number (optional). ³ Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.



PTO/SB/08B (10-01)
Approved for use through 10/31/2002. OMB 0851-0031
U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE
Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

+

Substitute for form 1449B/PTO		Complete if Known	
		Application Number	09/815,921
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Filing Date	March 24, 2001
		First Named Inventor	Bindiganavale S. Nataraj
		Group Art Unit	2661
		Examiner Name	Not Yet Assigned
		Attorney Docket Number	NLM.P002
Sheet	6	of	7

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
CLD		KLSI KE5B064A1 Address Processor Ver. 2.0.2 Preliminary (believed to be published late 1995 or early 1996).	
CLD		KLSI KE5B064H Series Address Processor Ver. 1.0.1 (believed to be published in late 1995 or early 1996).	
CLD		MASAYOSHI KOBAYASHI et al., "A 50Mpps Longest Prefix Match Search Engine LSI for Multigigabit IP Forwarding", Technical Report of IEICE IN08-119, November 1998, pp. 7-12.	
CLD		T.V. LAKSHMAN and D. STILIADIS, "High-Speed Policy-Based Packet Forwarding Using Efficient Multi-Dimensional Range Matching", SIGCOMM, 1988, pp. 203-214.	
CLD		LANDROCK et al., "Associative Storage Module", IBM Technical Disclosure Bulletin, Vol. 25, No. 5, October 1992, pp. 2341-2342.	
CLD		ANTHONY MCAULEY et al., "A Self-Testing Reconfigurable CAM", IEEE Journal of Solid-State Circuits, Vol. 26, No. 3, March 1991, pp. 257-261.	
CLD		Motorola Semiconductor Technical Data, "Advanced Information 16K X 64 CAM", MCM69C432, January 1996, 4 pages.	
CLD		MUSIC Semiconductors, "MU9C1480 LANCAM" Advance Information, March 22, 1990, pp. 1-11.	
CLD		MUSIC Semiconductors, "MU9C1480 LANCAM Handbook", November 1994.	
CLD		MUSIC Semiconductors, "MU9C2480 LANCAM" Preliminary Data Sheet, August 25, 1995, pp. 1-24.	
CLD		MUSIC Semiconductors, "MUAA CAM Family" Advanced Information, February 24, 1998, Rev. 0, pp. 1-16.	

RECEIVED

Technology Center 2600

AUG 14 2002

Examiner Signature		Date Considered	1/10/05
--------------------	--	-----------------	---------

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

+

